

PATENT APPLICATION  
DOCKET NO.: 01-C-050

**RULES 63 AND 67 (37 C.F.R. 1.63 and 1.67)**  
**DECLARATION AND POWER OF ATTORNEY**

**FOR UTILITY/DESIGN/CIP/PCT NATIONAL APPLICATIONS**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name; and

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: **CIRCUIT AND METHOD FOR TESTING A FERROELECTRIC MEMORY DEVICE**, the specification of which: (mark only one)

(a) is attached hereto.  
 (b) was filed on \_\_ as Application Serial No. \_\_ and was amended on \_\_\_\_ (if applicable)  
 (c) was filed as PCT International Application No. PCT/\_\_\_\_ on \_\_\_\_ and was amended on \_\_\_\_ (if applicable).  
 (d) was filed on \_\_ as Application Serial No. \_\_ and was issued a Notice of Allowance on \_\_\_\_.  
 (e) was filed on \_\_ and bearing attorney docket number \_\_\_\_

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims as amended by any amendment referred to above or as allowed as indicated above.

I acknowledge the duty to disclose all information known to me to be material to the patentability of this application as defined in 37 CFR § 1.56. If this is a continuation-in-part (CIP) application, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose to the Office all information known to me to be material to patentability of the application as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

I hereby claim foreign priority benefits under 35 U.S.C. § 119/365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate filed by me or my assignee disclosing the subject matter claimed in this application and having a filing date (1) before that of the application on which my priority is claimed or, (2) if no priority is claimed, before the filing date of this application:

PRIOR FOREIGN PATENTS

<u>Number</u>	<u>Country</u>	<u>Month/Day/Year Filed</u>	<u>Date first laid-open or Published</u>	<u>Date patented or Granted</u>	<u>Priority Claimed</u>
					<u>Yes</u> <u>No</u>
NONE					

I hereby claim the benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below:

PROVISIONAL APPLICATIONS

Application No. (series code/serial no.)    Month/Day/Year Filed

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I hereby claim the benefit under 35 U.S.C. § 120/365 of any United States application(s) listed below and PCT international applications listed above or below:

PRIOR U.S. OR PCT APPLICATIONS

Application No. (series code/serial no.)    Month/Day/Year Filed    Status(pending, abandoned, patented)

NONE

I hereby appoint the following attorneys and/or agents to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith: LISA K. JORGENSON, Reg. No. 34,845, ROBERT D. McCUTCHEON, Reg. No. 38,717, MARIO DONATO, Reg. No. 37,816, and NAINESH SHAH, Reg. No. 40,166, of STMicroelectronics, Inc. of 1310 Electronics Drive, Carrollton, Texas 75006-5039; and ANDRE M. SZUWALSKI, Reg. No. 35,701, GARY B. SOLOMON, Reg. No. 44,347, and WILLIAM F. ESSER, Reg. No. 38,053 of the firm of **JENKENS & GILCHRIST**, 3200 Fountain Place, 1445 Ross Avenue, Dallas, Texas 75202-2799;

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

**NAMED INVENTOR(S)**

<b>Full Name</b>	 Inventor's Signature	<b>Date</b>
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(FOR ADDITIONAL INVENTORS, check here  and add additional sheet for inventor information regarding signature, name, date, citizenship, residence and address)